

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ENTER DA 9-29-5 In re Application of:

Fee et al.

Serial No.: 09/933,297

Filed: August 20, 2001

For: QUAD FLAT NO-LEAD (QFN) GRID

ARRAY PACKAGE, METHOD OF

MAKING AND MEMORY MODULE AND

**COMPUTER SYSTEM** 

Confirmation No.: 5686

Examiner: D. Graybill

Group Art Unit: 2822

Attorney Docket No.: 2269-4738US

(00-1113.00/US)

Notice of Allowance Mailed:

June 2, 2005

NOTICE OF EXPRESS MAILING

Express Mail Mailing Label Number: <u>EL995993312US</u>

Date of Deposit with USPS: September 2, 2005

Person making Deposit: Steve Wong

## AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows: